## News of railML-Interlocking parts



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Paris, September 18th, 2013

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### Interlocking schema proposal

Status Quo

Overview

Feedback

Development

### Signal aspect integration

Generic concept

#### Outlook

Next steps in the Interlocking development

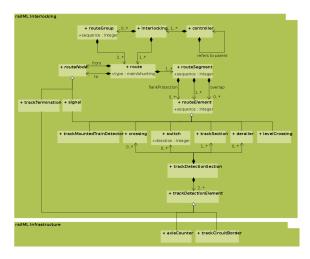
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## What happened?

- Last Interlocking workshop at June 13th, 2013 in Berlin
- Members: railML, Thales (DE, AT), Siemens (DE, NL, UK),
   Alstom, SBB, Infrabel, ON-TIME
- Open issues saved at the wiki page http://wiki.railml.org/index.php?title=IL\_ IntendedFeatures
- Conclusions implemented into the interlocking XML schemas
- Proposal XML schemas sent out via the IXL-Mailing list
- Test implementations: ON-TIME, Siemens-NL

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# Slightly outdated picture of the general Interlocking model



## Siemens UK / Invensys Rail opinion

"Generally speaking the news is very good. Aside from a few largely UK-specific points they really liked the model and also mentioned they'd be much more interested in using railML if interlocking was included (ETCS also came up...)." (John Easton, University of Birmingham, excerpt from an email)

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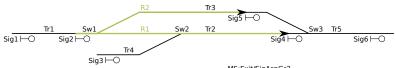
## Changes since last XML Schema proposal

- Integration of signal types and according signal aspects into the route definition
- Some bug fixes
- Enhanced wiki documentation

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Generic concept



DS/SigAspGr1 MS:Entry/SigAspGr2 MS:Entry/SigAspGr3 MS:Exit/SigAspGr3 MS:Exit/SigAspGr4

MS:BS/SigAspGr4

Signal Signal Signal aspect types aspects aroups DS caution SigAspGr1 caution. MS:Entry expect expect proceed proceed MS-Evit ston SigAspGr2 MS-RS reduced stop, reduced proceed 60, proceed reduced proceed 100. 60 proceed reduced proceed 100 SigAspGr3 ston. proceed reduced proceed 100

SigAspGr4

stop,

proceed

Signal aspect dependencies

SigAspDep1
MS:Entry → MS:Exit
stop → failed
reduced proceed 100 → stop
proceed

SigAspDep2

SigAspDep2
MS:Entry → MS:Exit
stop - failed
reduced proceed 60 → stop
reduced proceed 100

SigAspDep3

Routes

R1
Sig2 → Sig4: SigAspDep1
Tr1, Tr2
Sw1: right
Sw2: right
Sig1: SigAspDep3

R2
Sig2 → Sig5: SigAspDep2
Tr1, Tr3
Sw1: left
Sw1: SigAspDep3

Next steps in the Interlocking development

## How to go further?

- ▶ Next meeting on September 19th, 2013 at UIC in Paris
- Collect feedback on current proposal, discuss this
- Implement resulting clarifications and changes
- Discuss use cases and implementation strategy
- Discuss interaction with planned new railML infrastructure model (railML 3.0)

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Questions and discussion

## Thank you for your attention.



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